

**IN THE SPECIFICATION:**

[0031] As stated above, the combinatorial logic 180 employs the portion stored in the pseudo-memory 170 and corresponding data-out bit patterns read from the plurality of RAM arrays 120 to generate a response bit pattern that matches the probe bit pattern only if all of the data-out bit patterns match the probe bit pattern.

[0031] As stated above, the combinatorial logic 180 employs the portion stored in the pseudo-memory 170 and corresponding data-out bit patterns read from the plurality of RAM arrays 120 to generate a response bit pattern that matches the probe bit pattern only if all of the data-out bit patterns match the probe bit pattern.